

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (cancelled).

Claim 2 (currently amended): An apparatus for effecting symmetric driving of a write head as recited in Claim 3 wherein ~~said first drive unit comprises at least one first current mirror structure and said second drive unit comprises at least one second current mirror structure substantially similarly with said at least one first current mirror structure, and wherein~~ said at least one drive signal includes a direct current write current signal.

Claim 3 (currently amended): An apparatus for effecting symmetric driving of a write head; the apparatus comprising:

- (a) a first drive unit coupled with a first connection locus of said write head;
- (b) a second drive unit coupled with a second connection locus of said write head; and
- (c) a control unit coupled with said first drive unit and said second drive unit;

said control unit effecting complementary coordination by said first and second drive units to provide a first drive signal from said first drive unit at said first connection locus and a second drive signal from said second drive unit at said second connection locus, said first and second drive signals at least one drive signal in substantially equal magnitudes and of opposite polarities ~~at each of said first and second connection loci~~ during respective time intervals of operation of said write head,

wherein said first drive unit comprises at least one first current mirror structure and said second drive unit comprises at least one second current mirror structure substantially ~~similarly with~~ similar to said at least one first current mirror structure, and wherein said at least one drive signal includes a write boost current signal.

Claim 4 (currently amended): An apparatus for effecting symmetric driving of a write head; the apparatus comprising:

- (a) a first drive unit coupled with a first connection locus of said write head;
- (b) a second drive unit coupled with a second connection locus of said write head; and
- (c) a control unit coupled with said first drive unit and said second drive unit;

said control unit effecting complementary coordination by said first and second drive units to provide a first drive signal from said first drive unit at said first connection locus and a second drive signal from said second drive unit at said second connection locus, said first and second drive signals, at least one drive signal in substantially equal magnitudes and of opposite polarities ~~at each of said first and second connection loci~~ during respective time intervals of operation of said write head,

wherein said first drive unit comprises at least one first current mirror structure and said second drive unit comprises at least one second current mirror structure substantially ~~similarly with~~ similar to said at least one first current mirror structure, and wherein said at least one drive signal includes a direct current write current signal, and

wherein said at least one drive signal includes a write boost current signal.

Claim 5 (previously presented): An apparatus for effecting symmetric driving of a write head as recited in Claim 3 wherein said respective time intervals of operation are intervals of a digital data signal.

Claim 6 (original): An apparatus for effecting symmetric driving of a write head as recited in Claim 2 wherein said respective time intervals of operation are intervals of a digital data signal.

Claim 7 (cancelled)

Claim 8 (original): An apparatus for effecting symmetric driving of a write head as recited in Claim 4 wherein said respective time intervals of operation are intervals of a digital data signal.

Claim 9 (cancelled).

Claim 10 (currently amended): An apparatus for driving a write head in response to at least one data signal as recited in Claim 11 wherein said each of said first drive unit and said second drive unit ~~are substantially similar in construction and~~ comprise: a first logic level current mirror and a second logic level current mirror; each of said first and second logic level current ~~mirror~~ mirrors being coupled to receive a ~~logic level write drive component signal~~ direct current signal and responding to said control signals to present a representative ~~logic level write drive signal~~ direct current write current signal related to said ~~logic level write drive component signal~~ direct current signal to said write head in one of said first signal polarity or said second signal polarity.

Claim 11 (currently amended): An apparatus for driving a write head in response to at least one data signal; the apparatus comprising:

- (a) a first drive unit coupled with said write head;
- (b) a second drive unit coupled with said write head; and
- (c) a control unit coupled with said first drive unit and said second drive unit;

said control unit receiving said at least one data signal and generating control signals to said first drive unit and said second drive unit in response to

said at least one data signal; said control signals controlling said first drive unit to apply at least one first drive signal to a first write head connection locus of said write head in a first signal polarity and controlling said second drive unit to apply at least one second drive signal to a second write head connection locus of said write head in a second signal polarity opposite to said first signal polarity when said at least one data signal effects a signal excursion; said at least one first drive signal and said at least one second drive signal being substantially equal in magnitude; said at least one first drive signal and said at least one second drive signal being applied substantially simultaneously,

wherein each of said first drive unit and said second drive unit are substantially similar in construction and comprise: a first ~~boost~~ secondary current mirror and a ~~and second boost~~ secondary current mirror; each of said first and second ~~boost~~ secondary current ~~mirror~~ mirrors being coupled to receive a ~~boost current write drive component signal~~ boost signal; each respective ~~boost~~ secondary current mirror of said first and second ~~boost~~ secondary current ~~mirror~~ mirrors responding to said control signals to present ~~said boost current write drive component signal~~ a write boost current signal to said write head in the same signal polarity ~~of said first signal polarity or said second signal polarity~~ as said representative direct current write drive signal presented by said respective current mirror.

Claim 12 (currently amended): An apparatus for driving a write head in response to at least one data signal; the apparatus comprising:

- (a) a first drive unit coupled with said write head;
- (b) a second drive unit coupled with said write head; and
- (c) a control unit coupled with said first drive unit and said second drive unit;

said control unit receiving said at least one data signal and generating control signals to said first drive unit and said second drive unit in response to said at least one data signal; said control signals controlling said first drive unit to apply at least one first drive signal to a first write head connection locus of said

write head in a first signal polarity and controlling said second drive unit to apply at least one second drive signal to a second write head connection locus of said write head in a second signal polarity opposite to said first signal polarity when said at least one data signal effects a signal excursion; said at least one first drive signal and said at least one second drive signal being substantially equal in magnitude; said at least one first drive signal and said at least one second drive signal being applied substantially simultaneously,

wherein said each of said first drive unit and said second drive unit are substantially similar in construction and comprise: a first logic level current mirror and a second logic level current mirror; each of said first and second logic level current mirror ~~mirrors~~ being coupled to receive a logic-level write drive component signal direct current signal and responding to said control signals to present a representative logic level write drive signal related to said ~~logic-level write drive component signal~~ direct current signal to said write head in one of said first signal polarity or said second signal polarity, and

wherein each of said first drive unit and said second drive unit are substantially similar in construction and comprise: a first ~~boost~~ secondary current mirror and a ~~and second boost~~ secondary current mirror; each of said first and second ~~boost~~ secondary current ~~mirror~~ mirrors being coupled to receive a ~~boost current write drive component signal~~ boost signal; each respective ~~boost~~ secondary current mirror of said first and second ~~boost~~ secondary current mirror ~~mirrors~~ responding to said control signals to present ~~said boost current write drive component signal~~ a write boost current signal to said write head in the same signal polarity of ~~said first signal polarity or said second signal polarity~~ as said representative ~~direct current~~ logic level write drive signal presented by said respective current mirror.

Claim 13 (cancelled).

Claim 14 (currently amended): A method for driving a write head in response to at least one data signal as recited in Claim 15 wherein each of said first drive unit

and said second drive unit ~~are substantially similar in construction and comprise:~~
a first logic level current mirror and a second logic level current mirror; each of
said first and second logic level current mirror being coupled to receive a logic
level write drive component signal and responding to said control signals to
present a representative logic level write drive signal related to said logic level
write drive component signal to said write head in one of said first signal polarity
or said second signal polarity.

Claim 15 (currently amended): A method for driving a write head in response to
at least one data signal; the method comprising the steps of:

(a) in no particular order:

- (1) providing a first drive unit coupled with said write head;
- (2) providing a second drive unit coupled with said write head; and
- (3) providing a control unit coupled with said first drive unit and said

second drive unit; and

(b) operating said control unit to receive said at least one data signal and
generate control signals to said first drive unit and said second drive unit in
response to said at least one data signal; said control signals controlling said first
drive unit to apply at least one first drive signal to a first write head connection
locus of said write head in a first signal polarity and controlling said second drive
unit to apply at least one second drive signal to a second write head connection
locus of said write head in a second signal polarity opposite to said first signal
polarity when said at least one data signal effects a signal excursion; said at least
one first drive signal and said at least one second drive signal being substantially
equal in magnitude; said at least one first drive signal and said at least one
second drive signal being applied substantially simultaneously,

wherein each of said first drive unit and said second drive unit are
substantially similar in construction and comprise: a first ~~boost~~ secondary current
mirror and a ~~and second boost~~ secondary current mirror; each of said first and
second ~~boost~~ secondary current ~~mirror~~ mirrors being coupled to receive a ~~boost~~
~~current write drive component signal~~ boost signal; each respective ~~boost~~

~~secondary~~ current mirror of said first and second ~~boost secondary~~ current mirror ~~mirrors~~ responding to said control signals to present said ~~boost current write drive component signal~~ a write boost current signal to said write head in the same signal polarity of ~~said first signal polarity or said second signal polarity~~ as said representative ~~direct current logic level~~ write drive signal presented by said respective current mirror.

Claim 16 (currently amended): A method for driving a write head in response to at least one data signal; the method comprising the steps of:

(a) in no particular order:

- (1) providing a first drive unit coupled with said write head;
- (2) providing a second drive unit coupled with said write head; and
- (3) providing a control unit coupled with said first drive unit and said

second drive unit; and

(b) operating said control unit to receive said at least one data signal and generate control signals to said first drive unit and said second drive unit in response to said at least one data signal; said control signals controlling said first drive unit to apply at least one first drive signal to a first write head connection locus of said write head in a first signal polarity and controlling said second drive unit to apply at least one second drive signal to a second write head connection locus of said write head in a second signal polarity opposite to said first signal polarity when said at least one data signal effects a signal excursion; said at least one first drive signal and said at least one second drive signal being substantially equal in magnitude; said at least one first drive signal and said at least one second drive signal being applied substantially simultaneously,

wherein each of said first drive unit and said second drive unit are substantially similar in construction and comprise: a first logic level current mirror and a second logic level current mirror; each of said first and second logic level current mirror ~~mirrors~~ being coupled to receive a ~~logic-level write drive component signal~~ direct current signal and responding to said control signals to present a representative logic level write drive signal related to said ~~logic-level~~

write drive component signal direct current signal to said write head in one of said first signal polarity or said second signal polarity, and

wherein each of said first drive unit and said second drive unit are ~~substantially similar in construction and~~ comprise: a first ~~boost~~ secondary current mirror and a ~~and~~ second ~~boost~~ secondary current mirror; each of said first and second ~~boost~~ secondary current mirror ~~mirrors~~ being coupled to receive a ~~boost~~ current write drive component signal boost signal; each respective ~~boost~~ secondary current mirror of said first and second ~~boost~~ secondary current mirror ~~mirrors~~ responding to said control signals to present said ~~boost current write drive component signal~~ a write boost current signal to said write head in the same signal polarity of said first signal polarity or said second signal polarity as said representative ~~direct current~~ logic level write drive signal presented by said respective current mirror.